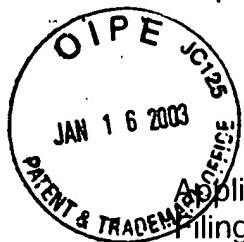


EV182656694



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/512,149  
Filing Date ..... February 23, 2000  
Inventor ..... Vishnu K. Agarwal  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2814  
Examiner ..... M. Pizarro Crespo  
Attorney's Docket No. .... MI22-1322  
Title: Integrated Circuitry Including a Capacitor with an Amorphous and a Crystalline High K Capacitor Dielectric Region

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

Assistant Commissioner for Patents  
Washington, D.C. 20231

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449 and copies of which are attached. No admission is made regarding whether the submitted references are prior art.

Citation of these references is respectfully requested.

Respectfully submitted,

Date: 1-16-03

By: D. Brent Kenady

D. Brent Kenady  
Reg. No. 40,045  
WELLS ST. JOHN P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828  
(509) 624-4276

01/21/2003 GGE REGI 00000032 09512149

01 FC:1806

180.00 OP